

Clock Oscillator (LVDS)

SCO-2520D



General Description

Ceramic packaged oscillator with differential output and very high frequency range available

Features

Excellent low phase noise and Jitter
Very High frequency range available

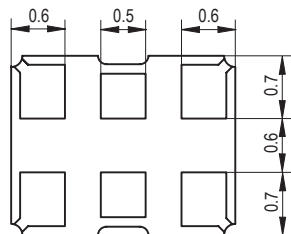
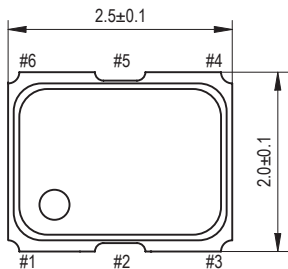
Main applications

Fibre Channel, Giga Ethernet, ATA, SCSI, PCI-Express, SDH/SONET

Electrical characteristics

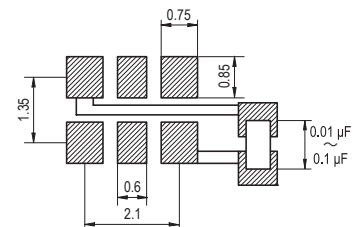
Item	Values
Part number	SCO-2520D LVDS
Output Waveform	SQUARED DIFFERENTIAL
Supply voltage	+2.5V , +3.3V
Frequency range	6.0 - 175 MHz
Current consumption	40mA max
Operating temperature range	-20°C / +70°C or -40 / +85°C
Storage temperature range	-55°C / +125°C
Frequency stability	<±50ppm overall or user spec
Output load	100Ω (OUT-OUTN)
Duty cycle	40%...60%
Output High Level	1.43V typ. - 1.6V max
Output Low level	0.9V min - 1.1V typ.
Differential output voltage	0.247V - 0.350V - 0.454V
Offset voltage	1.125V - 1.250V - 1.375V
Option Enable-Disable/Tri-state	available (Enable @ >70% Vcc , Disable at <30% Vcc)
Start up time	5ms max
Phase Jitter (12KHz - 20MHz)	0.5ps RMS max
Aging	<±3ppm / year

Dimensions



PIN	CONNECTION	
	1	"L"
2	N.C.	
3	GND	
4	Z	OUTPUT
5	Z	C- OUTPUT
6	V _{DD}	

Z : high impedance



Clock Oscillator (LVDS)

SCO-2520D

Part Number Generator

SCO-2520D 0 A 50 - 010.000000

0 1 2 3 4

0 : Type
SCO-2520D

1 : Vcc
2 = +2.5V
3 = +3.3V

2: Op. temp. range
blank = -20/+70
A=-40/+85

3 : Stability overall
50 < ±50ppm
25 < ±25ppm
20 < ±20ppm

4 : Frequency (MHz)
□□□.□□□□□□
max 10 digits including comma

Remark : specifications subject to change without prior notice. Please confirm with our sales

