

Clock Oscillator (LVDS)

SCO-7050D



General Description

Ceramic packaged oscillator with differential output and very high frequency range available

Features

Excellent low phase noise and Jitter
Very High frequency range available

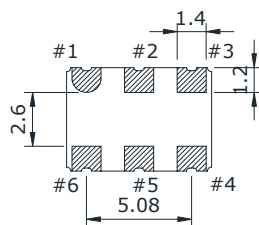
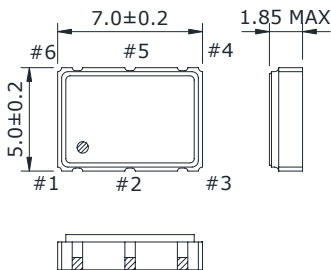
Main applications

Fibre Channel, Giga Ethernet, ATA, SCSI, PCI-Express, SDH/SONET

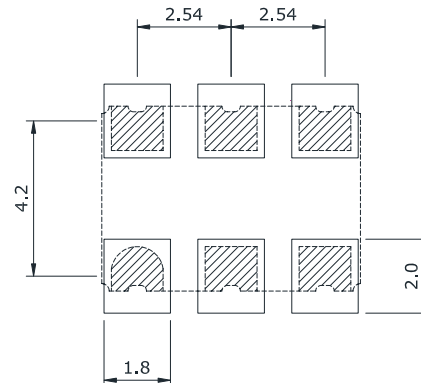
Electrical characteristics

Item	Values	
Part number	SCO-7050D LVDS	
Output Waveform	SQUARED DIFFERENTIAL	
Supply voltage	+2.5V , +3.3V	+3.3V
Frequency range	0.75 - 250 MHz	250 - 800 MHz (PLL)
Current consumption	65mA max	85mA max
Operating temperature range	-20°C / +70°C or -40 / +85°C	
Storage temperature range	-55°C / +125°C	
Frequency stability	<±50ppm overall or user spec	
Output load	100Ω (OUT-OUTN)	
Duty cycle	40%..60%	
Output High Level	1.43V typ. - 1.6V max	
Output Low level	0.9V min - 1.1V typ.	
Differential output voltage	0.247V - 0.350V - 0.454V	
Offset voltage	1.125V - 1.250V - 1.375V	
Option Enable-Disable/Tri-state	available (Enable @ >70% Vcc , Disable at <30% Vcc)	
Start up time	5ms max	
Phase Jitter (12KHz - 20MHz)	1ps RMS max	3.5ps RMS max
Aging	<±3ppm / year	

Dimensions



CONNECTION
 #1 : Tri-state
 #2 : NC
 #3 : GND
 #4 : OUTPUT(Q)
 #5 : OUTPUT(Q)
 #6 : VDD



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SCO-7050D

Part Number Generator

SCO-7050D 0 A 50 - 010.000000

0 1 2 3 4

0 : Type
SCO-7050D

1 : Vcc
2 = +2.5V
3 = +3.3V

2 : Op. temp. range
blank = -20/+70
A=-40/+85

3 : Stability overall
50 < ±50ppm
25 < ±25ppm
20 < ±20ppm

4 : Frequency (MHz)
□□□.□□□□□□
max 10 digits including comma