

Clock Oscillator (LVPECL)

SCO-7050P



General Description

Ceramic packaged oscillator with differential output and very high frequency range available

Features

Excellent low phase noise and Jitter
Very High frequency range available

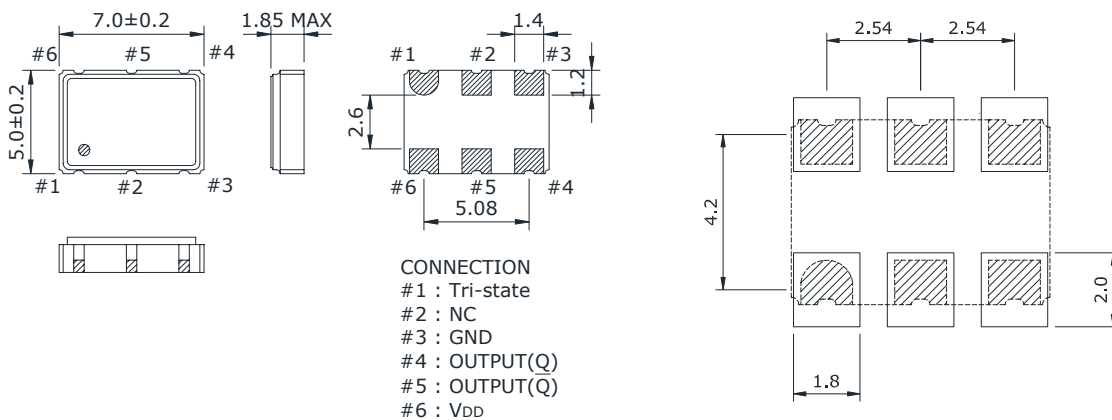
Main applications

Fibre Channel, Giga Ethernet, ATA, SCSI, PCI-Express, SDH/SONET

Electrical characteristics

Item	Values	
Part number	SCO-7050P LVPECL	
Output Waveform	SQUARED DIFFERENTIAL	
Supply voltage	+2.5V , +3.3V	+3.3V
Frequency range	0.75 - 250 MHz	250 - 800 MHz (PLL)
Current consumption	80mA max	100mA max
Operating temperature range	-20°C / +70°C or -40 / +85°C	
Storage temperature range	-55°C / +125°C	
Frequency stability	<±50ppm overall or user spec	
Output load	50Ω (terminated to Vdd-2V)	
Duty cycle	40%..60%	
Output High Level	1.415V - 1.760V (2.5Vdd) , 2.215 - 2.42V (3.3Vdd)	
Output Low level	0.67V - 1.195V (2.5Vdd) , 1.47V - 1.745V (3.3Vdd)	
Option Enable-Disable/Tri-state	available (Enable @ >70% Vcc , Disable at <30% Vcc)	
Start up time	5ms max	
Phase Jitter (12KHz - 20MHz)	1ps RMS max	3.5ps RMS max
Aging	<±3ppm / year	

Dimensions



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Part Number Generator

SCO-7050P 0 A 50 - 010.000000

0 1 2 3 4

0 : Type
SCO-7050P

1 : Vcc
2 = +2.5V
3 = +3.3V

2 : Op. temp. range
blank = -20/+70
A=-40/+85

3 : Stability overall
50 < ±50ppm
25 < ±25ppm
20 < ±20ppm

4 : Frequency (MHz)
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max 10 digits including comma

